99. (Previously Presented) The semiconductor structure of claim 98, wherein said photo-

definable layer comprises plasma polymerized methylsilane (PPMS).

100. (Previously Presented) The semiconductor structure of claim 99, wherein said conductive

layer forms an interconnect structure within a semiconductor memory device.

REMARKS

Status of Claims

Claims 1-54 have been canceled. Claims 55, 59, 62, 71, 84, and 97 have been amended.

Claims 55-100 are pending in the case.

Conclusion

The examiner is invited to contact the undersigned at the phone number indicated below

with any questions or comments, or to otherwise facilitate expeditious and compact prosecution

of the application.

Respectfully submitted,

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